

ABSTRACT OF THE DISCLOSURE

INTEGRATED CIRCUIT INCLUDING ACTIVE COMPONENTS
AND AT LEAST ONE PASSIVE COMPONENT AND ASSOCIATED
FABRICATION METHOD

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There is provided an integrated circuit having active components including junctions formed in a monocrystalline substrate doped locally, and at least one passive component situated above the active components. The integrated circuit includes a first insulating layer separating the active components and a base of the passive component, and a metal terminal for electrically connecting the passive component with at least one of the active components. The metal terminal is formed in the thickness of the first insulating layer and has a contact surface that projects from the limits of a junction of the one active component. In a preferred embodiment, the passive component is a capacitor. Also provided is a method of fabricating an integrated circuit that includes MOS transistors and an onboard memory plane of DRAM cells in a matrix.

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